Code No: 135BM/125EM/115EM/55029		R16/R15/R13/550)29
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD			AD
	B. Tech III Year I Semester Exa		
	SOFTWARE ENG		
	(R16-Common to CSE, IT; R15-Common to R09-Common to		Γ;
Time:	3 Hours	Max. M	larks: 75
	Answer any five of	Juestions	
	All questions carry e	equal marks	
1.	"The software is an integral part of every human's life". Substantiate the statement w		
	suitable discussions on evolving role of softw	vare.	[15]
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2.	What is unified process? Explain the four phases and work flows associated with the unified process.		
	unned process.		[15]
3.a)	What does feasibility analysis imply when	it is discussed within the con-	text of the
5.4)) What does feasibility analysis imply when it is discussed within the context o inception function?		
b)	How to validate requirements? Illustrate.		[8+7]
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4.	Demonstrate the steps performed by analyst	n creating a behavioral model.	[15]
5.a)	Describe about the design of conventional co		
b)	Differentiate between an architectural pattern	and a framework.	[7+8]
6.	Compare and contrast the design steps for tra	nsaction mapping and steps for tra	
	mapping.		[15]
7 a)	Explain about the art of Debugging.		
7.a) b)	Explain about the metrics for software quality	X 7	[7+8]
0)	Explain about the methos for software quality	y •	[, 0]
8.a)	Differentiate between risk components and ri	sk drivers.	
b)	Explain about the software quality assurance		[7+8]
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